



ATTORNEY DOCKET NO. R. PULLELA 4-4

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Rajasekhar Pullela
Serial No.: 10/033,525
Filed: December 28, 2001
Title: TRANS-ADMITTANCE TRANS-IMPEDANCE
LOGIC FOR INTEGRATED CIRCUITS
Grp./A.U.: 2816
Examiner: Dinh Thanh Le

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313, on	
04/15/2004	(Date)
S. R. PULLELA	
(Printed or typed name of person signing the certificate)	
S. R. PULLELA	
(Signature of the person signing the certificate)	

PRELIMINARY AMENDMENT

Before examination of the above-identified application, please amend it as follows and refer to the below arguments: